



TITLE: METHOD OF FABRICATING A TRANSISTOR ON A SUBSTRATE TO OPERATE AS A FULLY DEPLETED STRUCTURE

INVENTOR'S NAME: Leonard Forbes, et al.

SERIAL NO.: 10/057,225 DOCKET NO.: 303.506US4

1/26

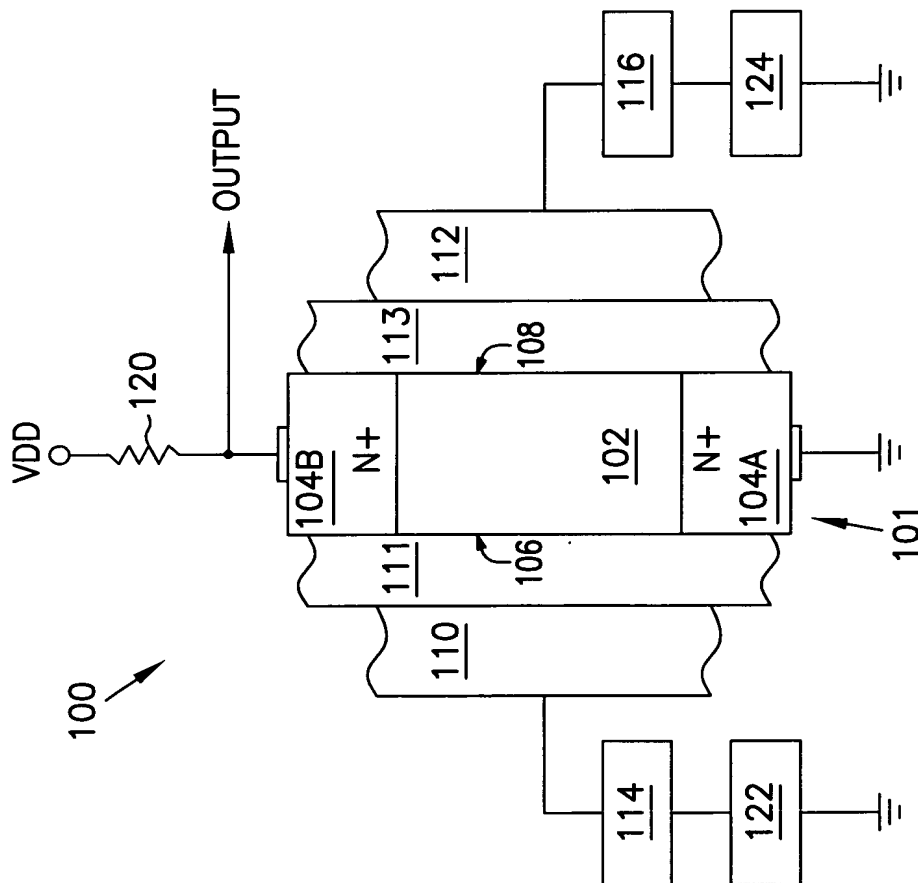


FIG. 1A

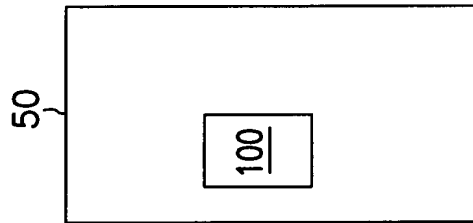


FIG. 1B

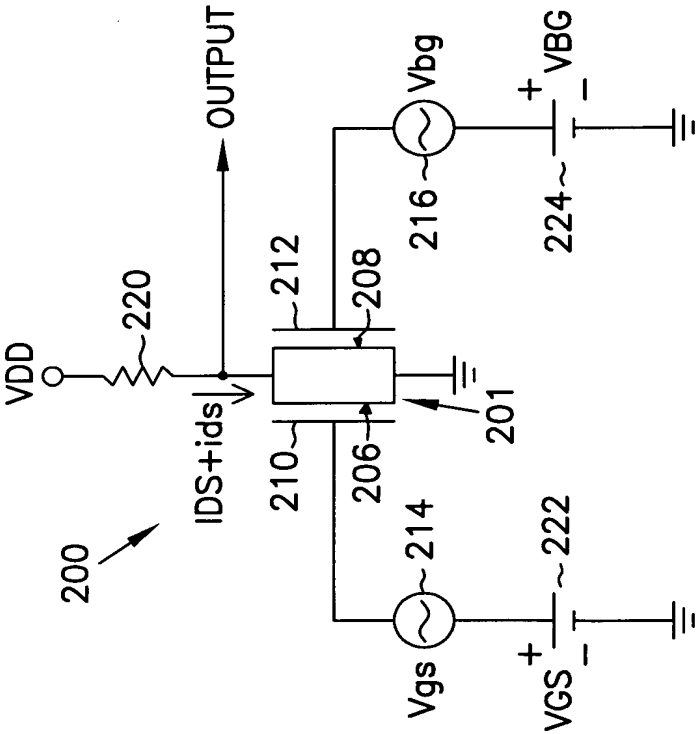


FIG. 2

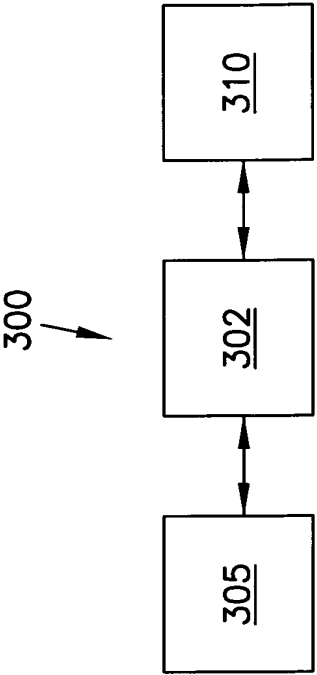


FIG. 3



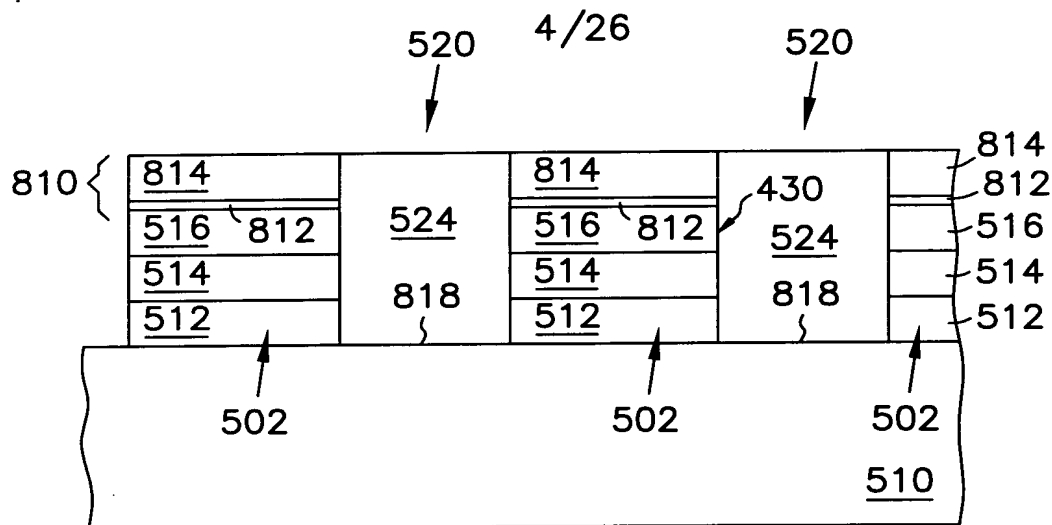


FIG. 4D

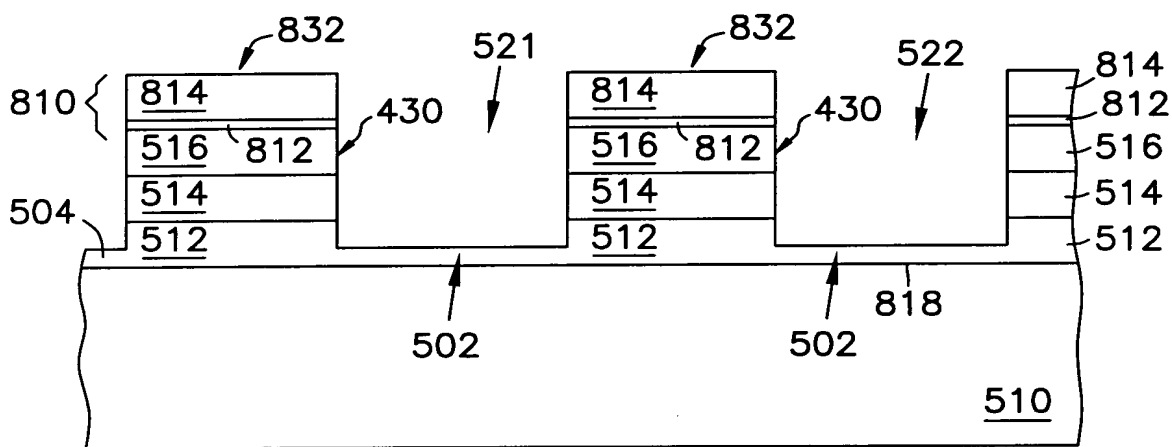


FIG. 4E

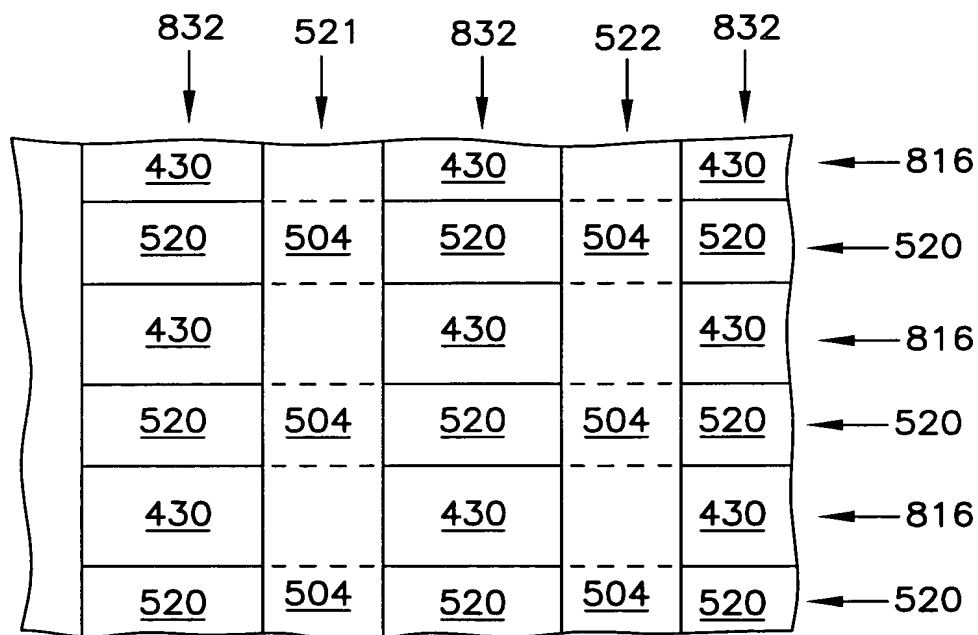


FIG. 4F

5/26

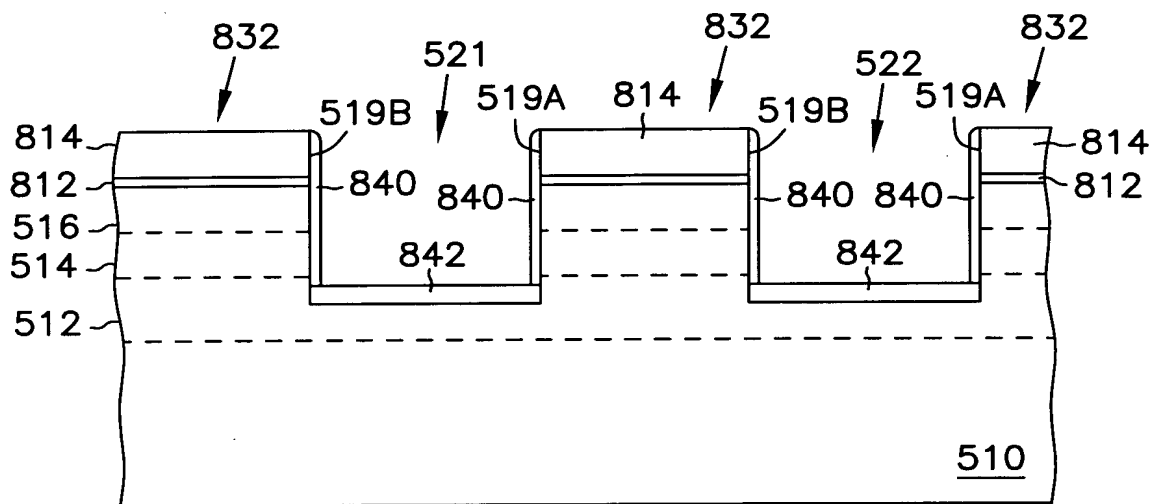


FIG. 4G

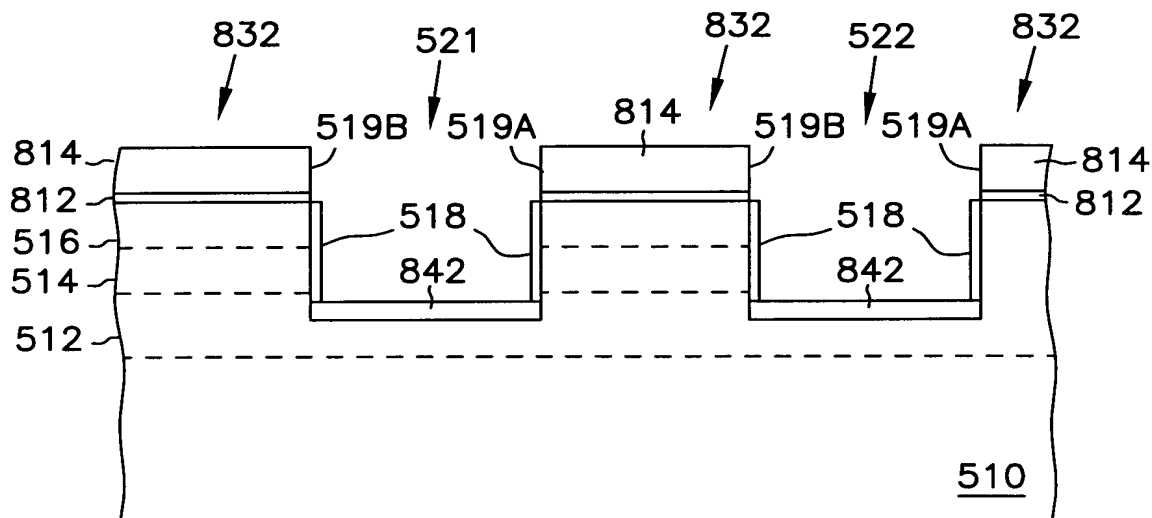


FIG. 4H

6/26

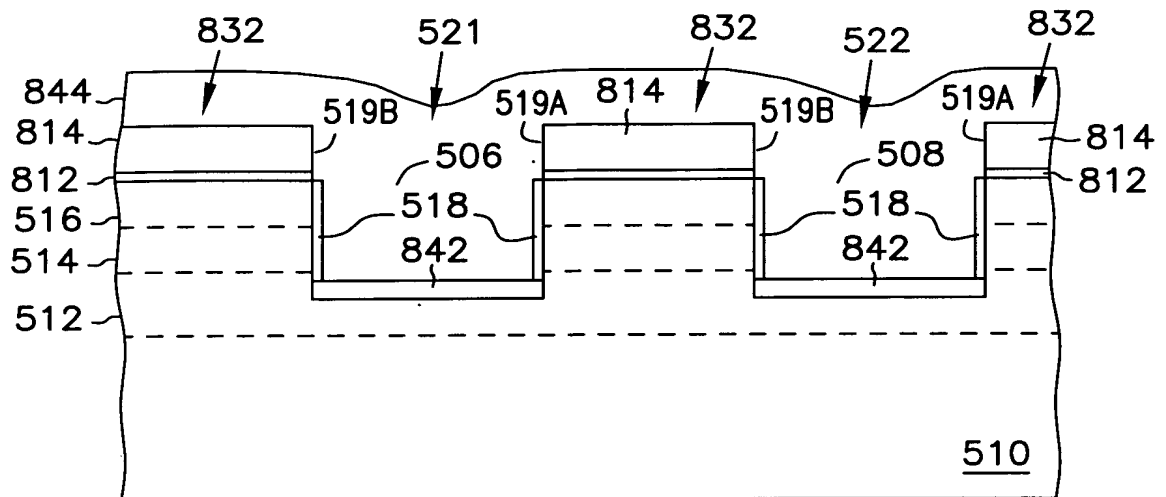


FIG. 4I

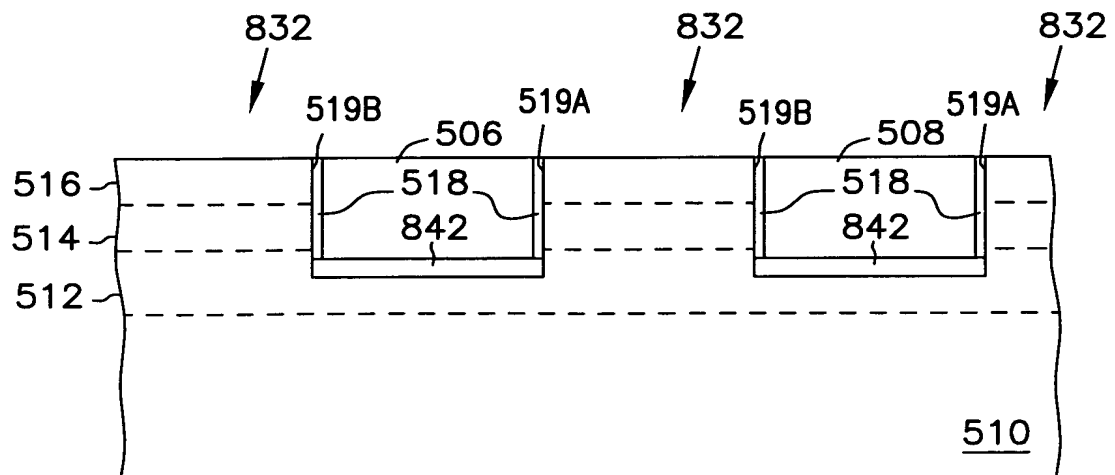


FIG. 4J

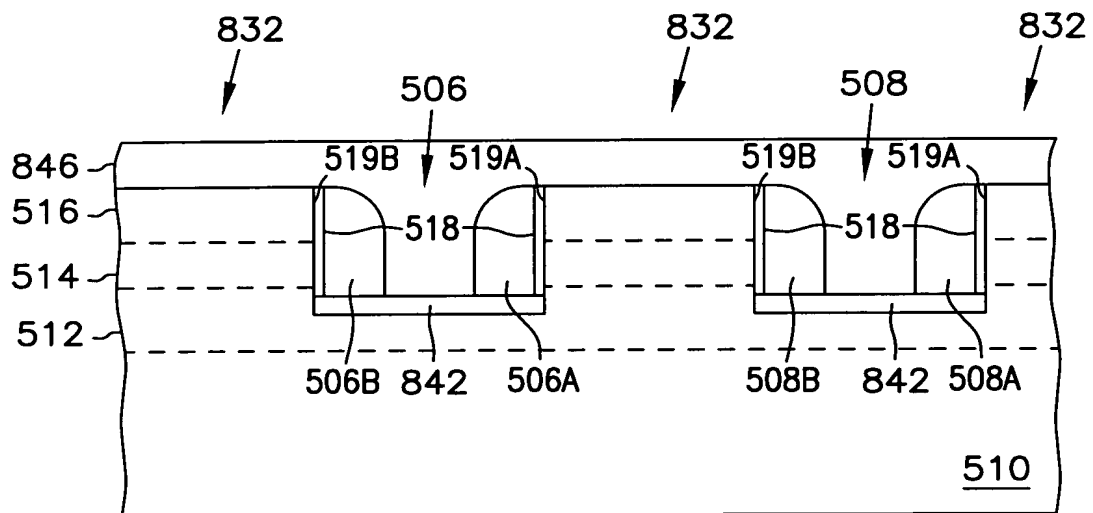


FIG. 4K

8/26

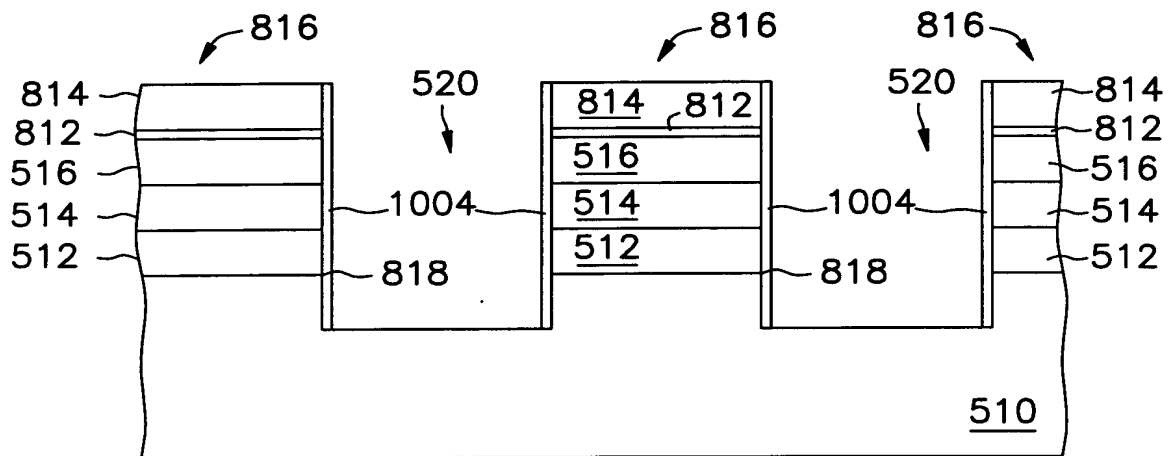


FIG. 5A

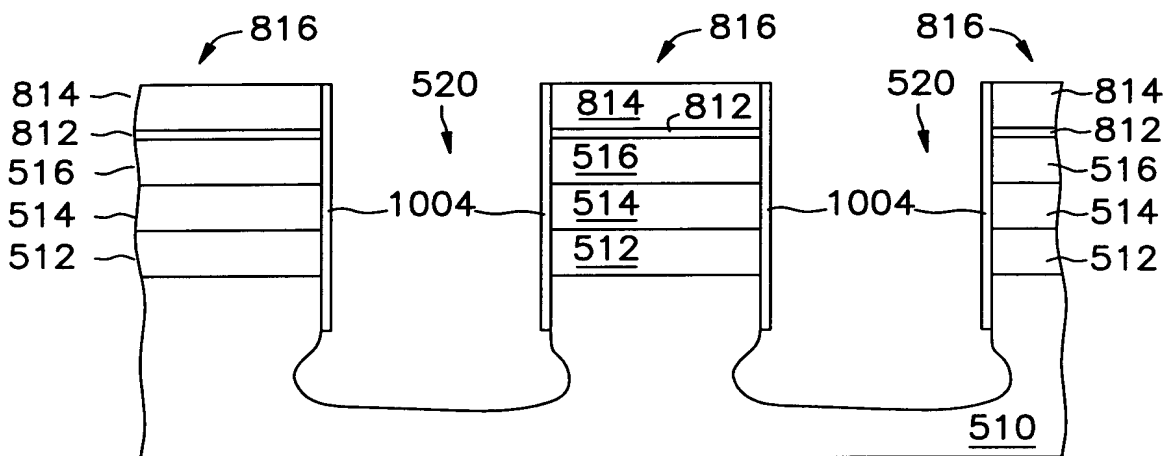


FIG. 5B

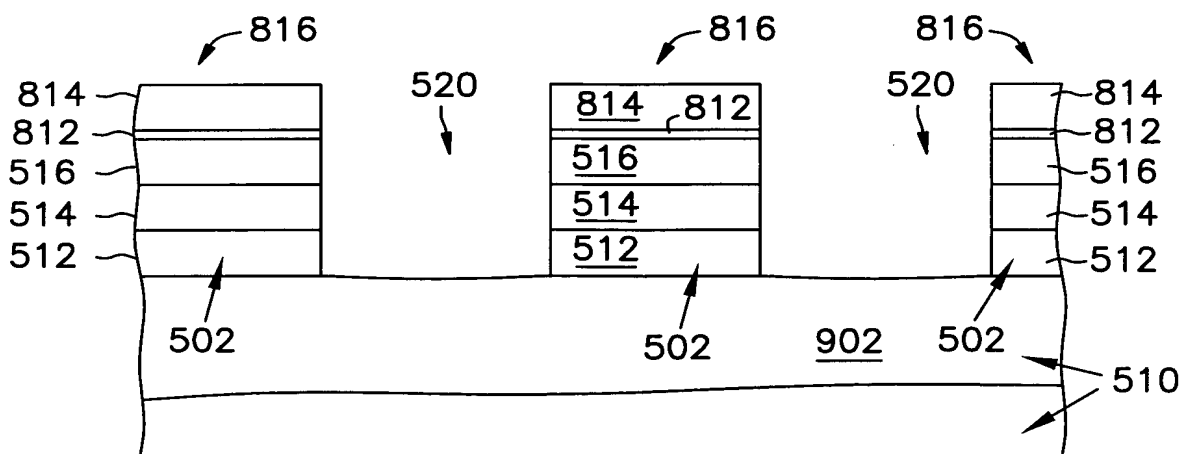


FIG. 5C

9/26

FIG. 6A

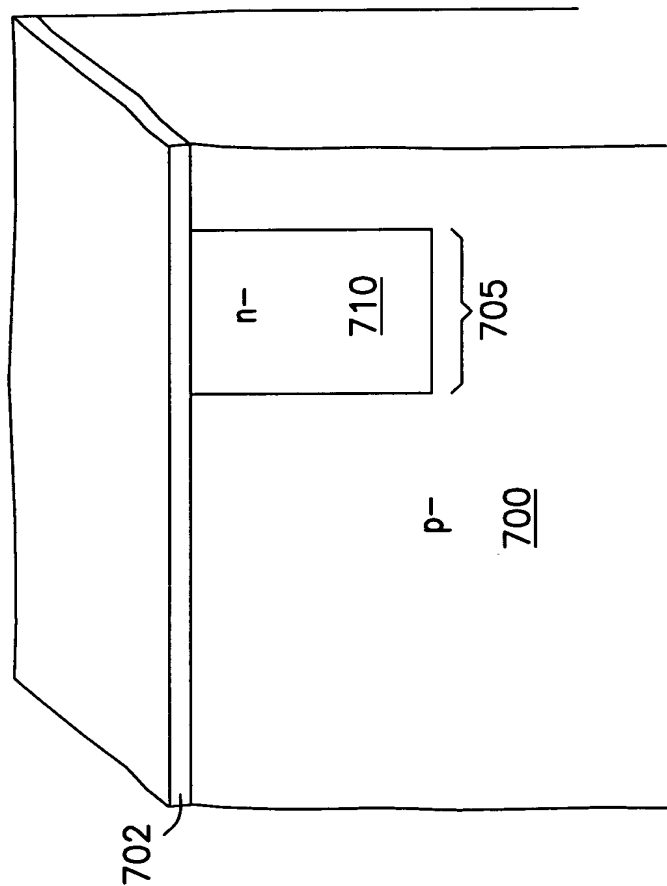
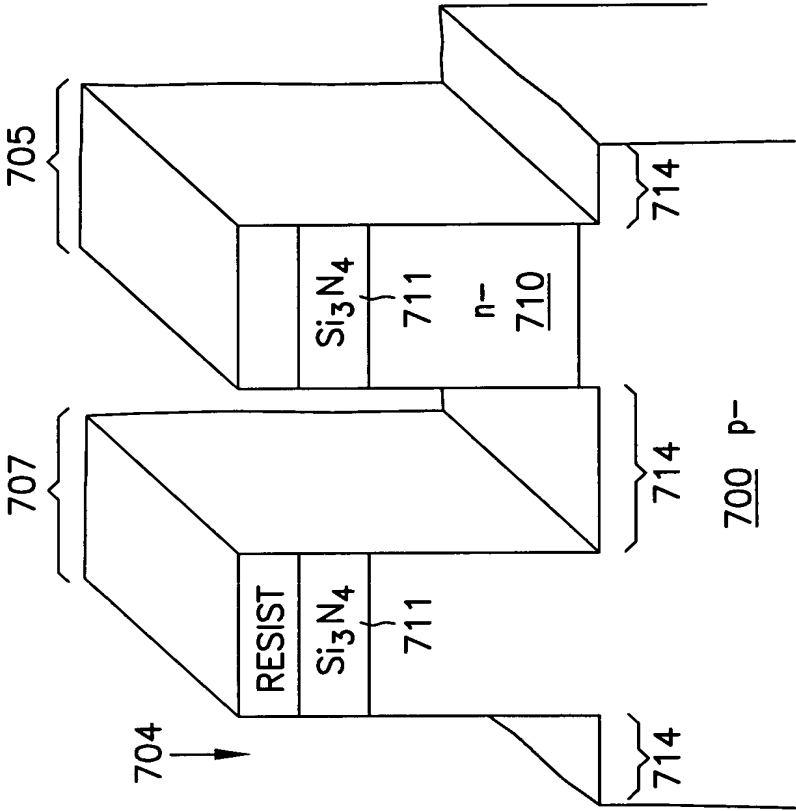


FIG. 6B



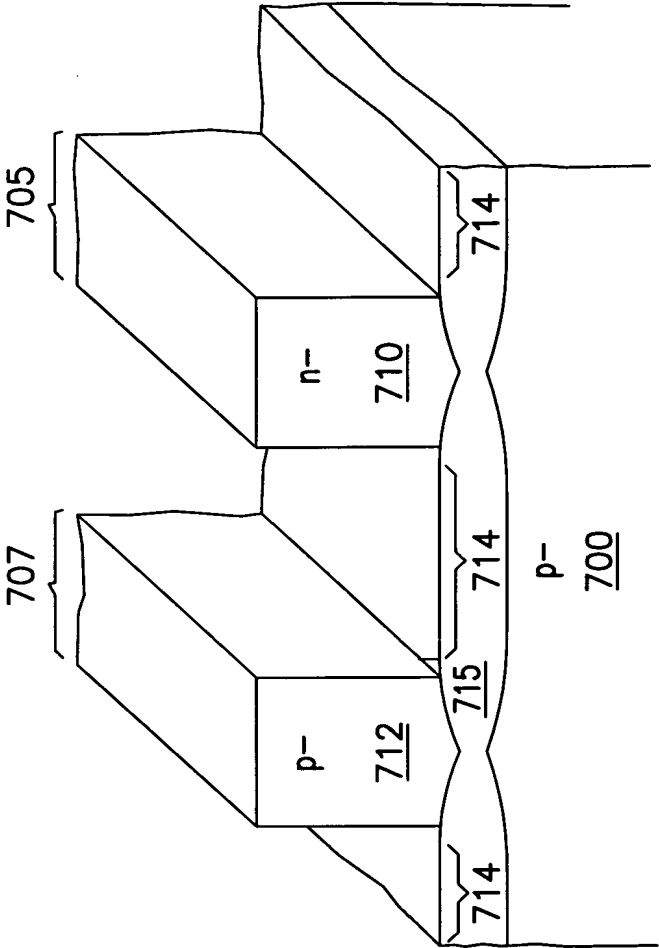
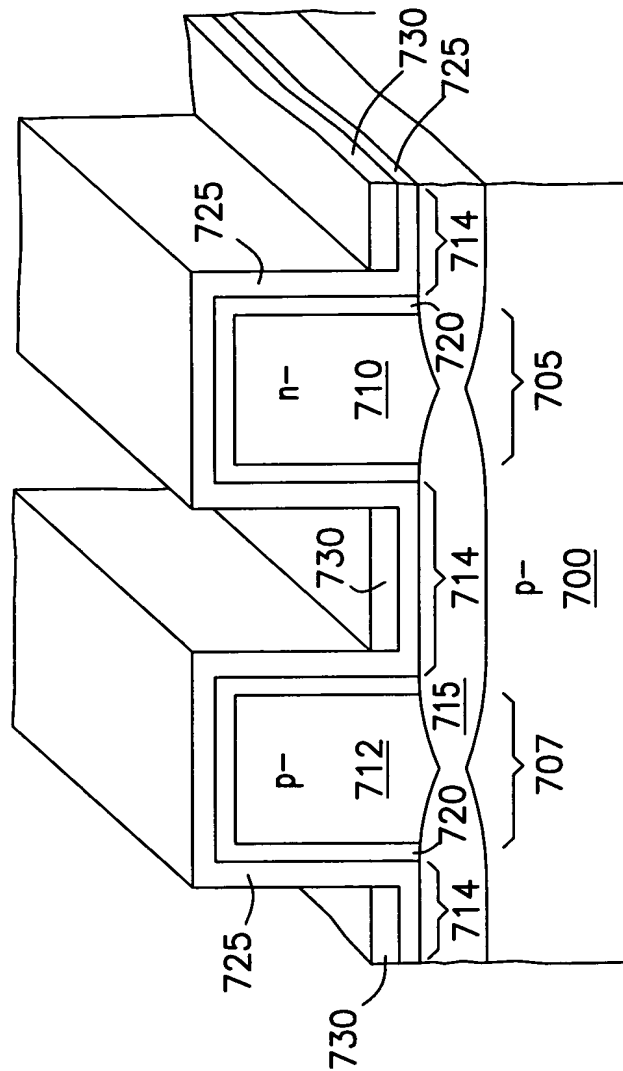


FIG. 6C

12/26

FIG. 6D



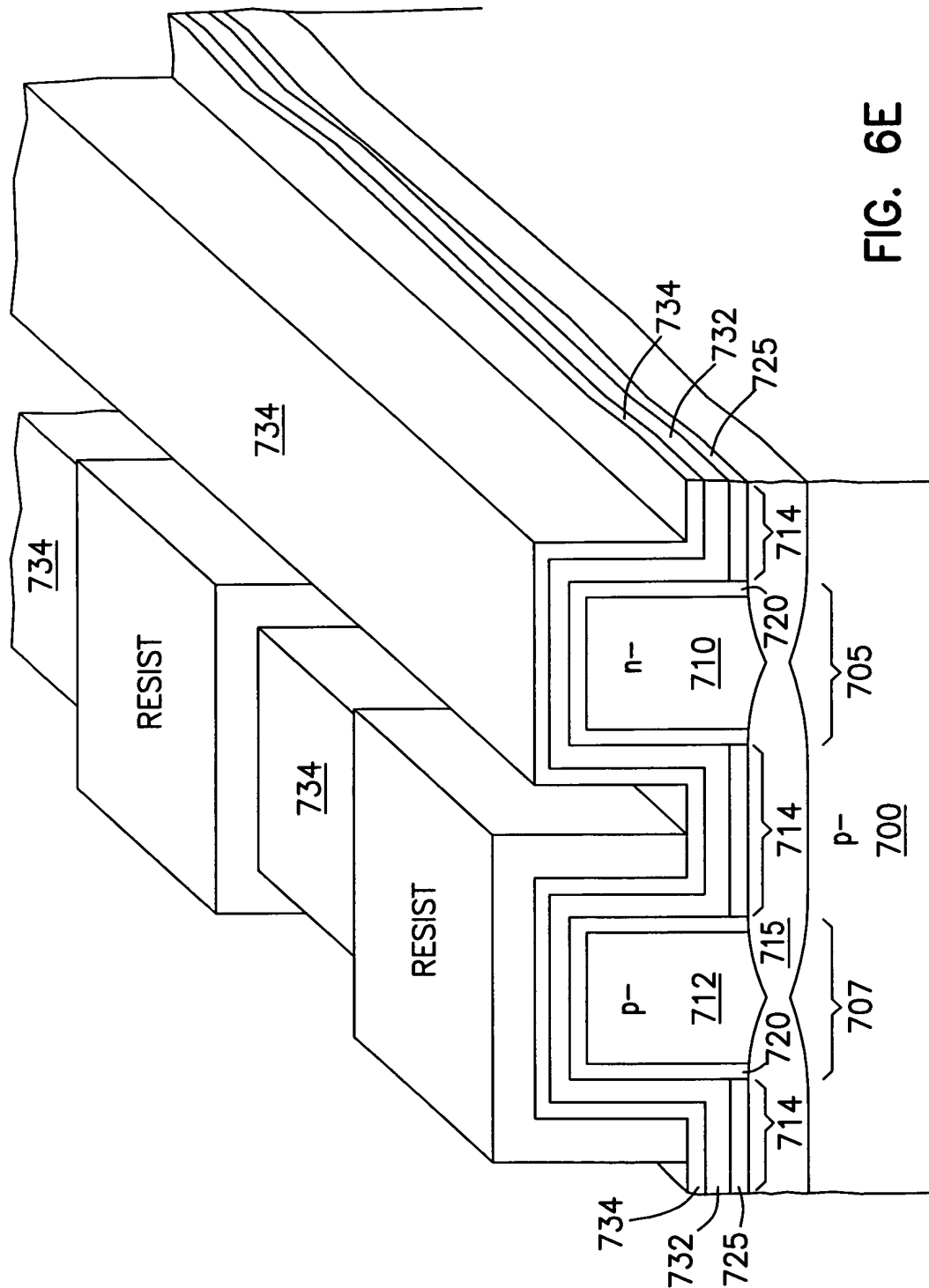
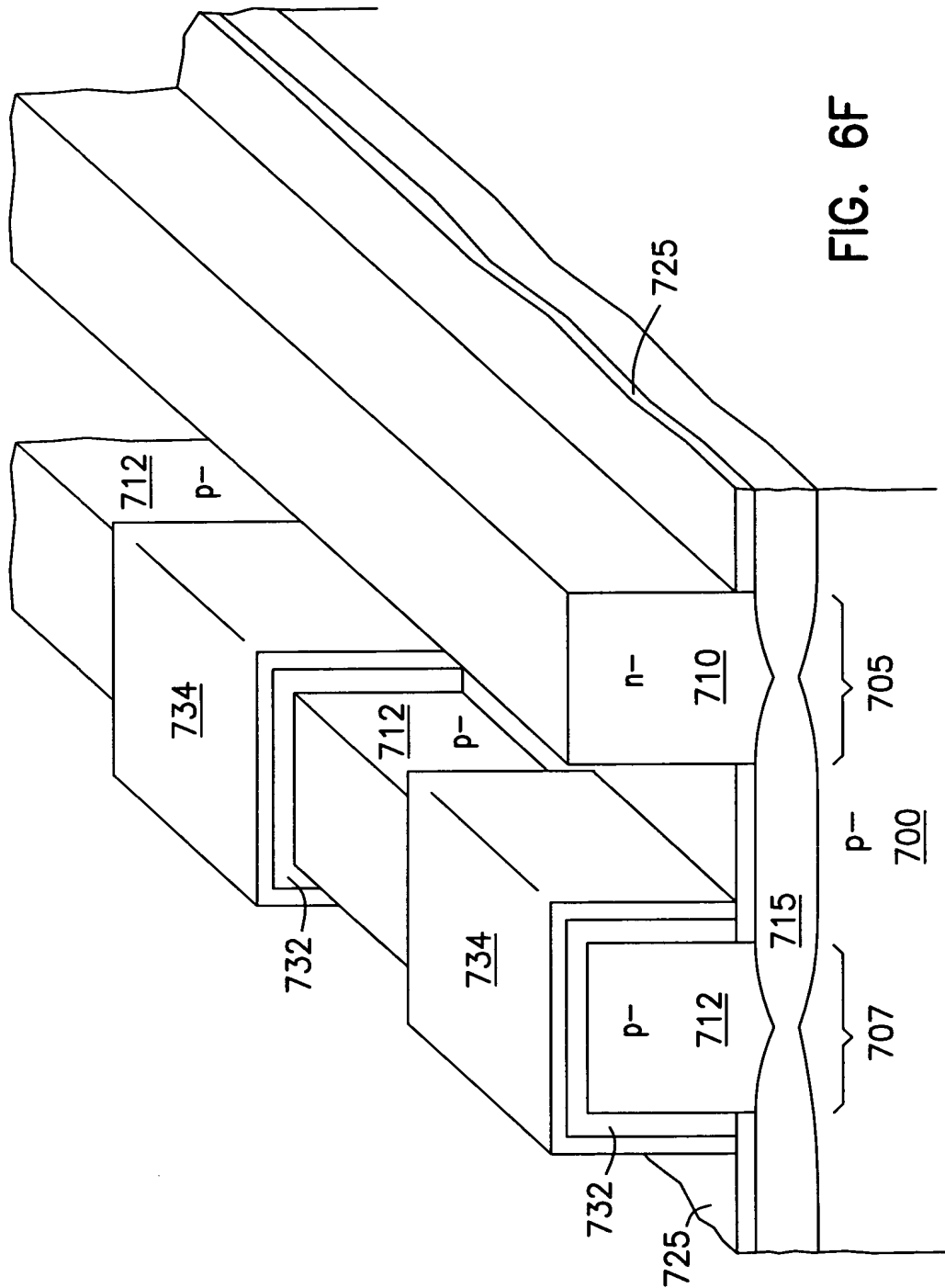
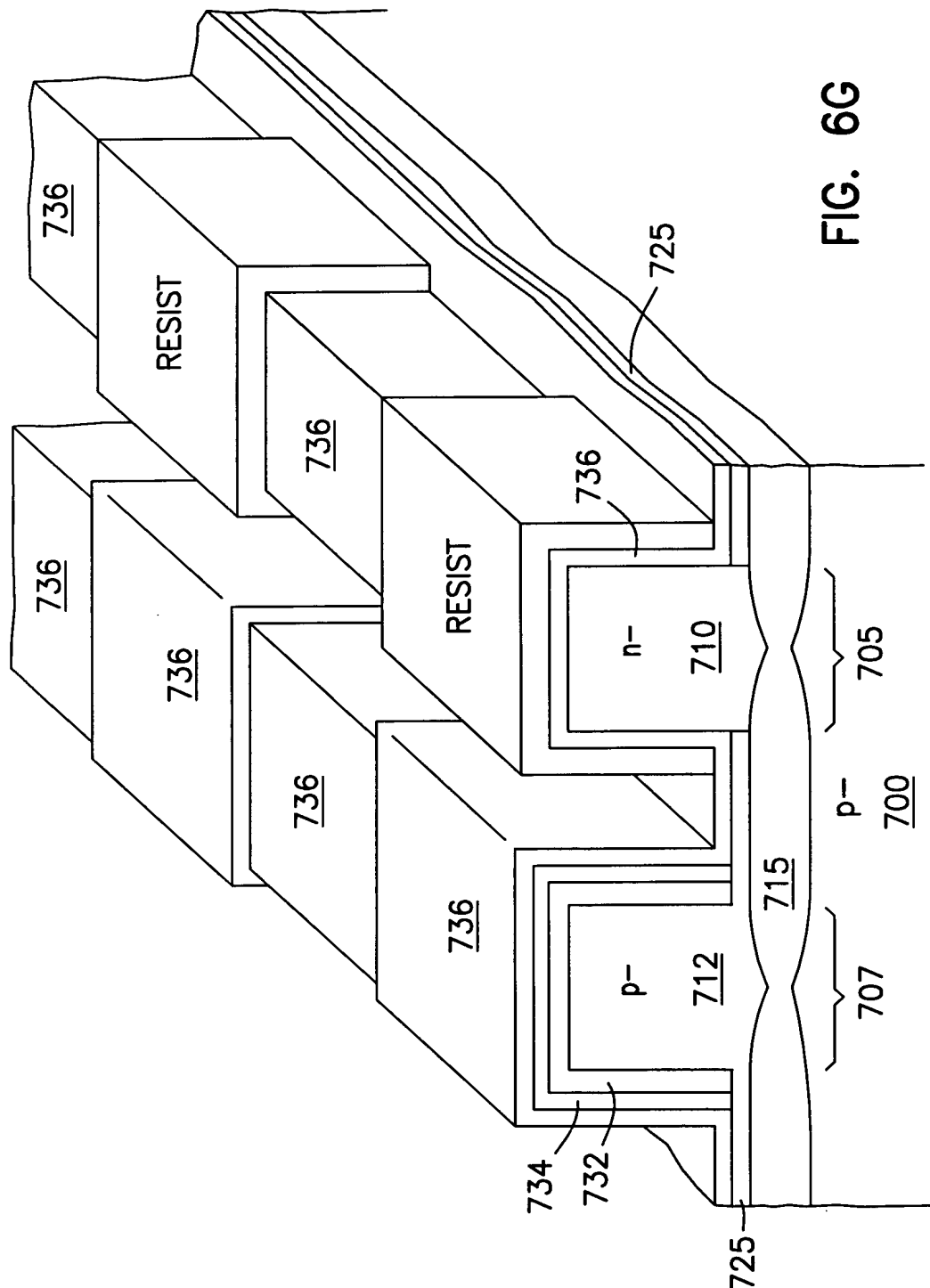
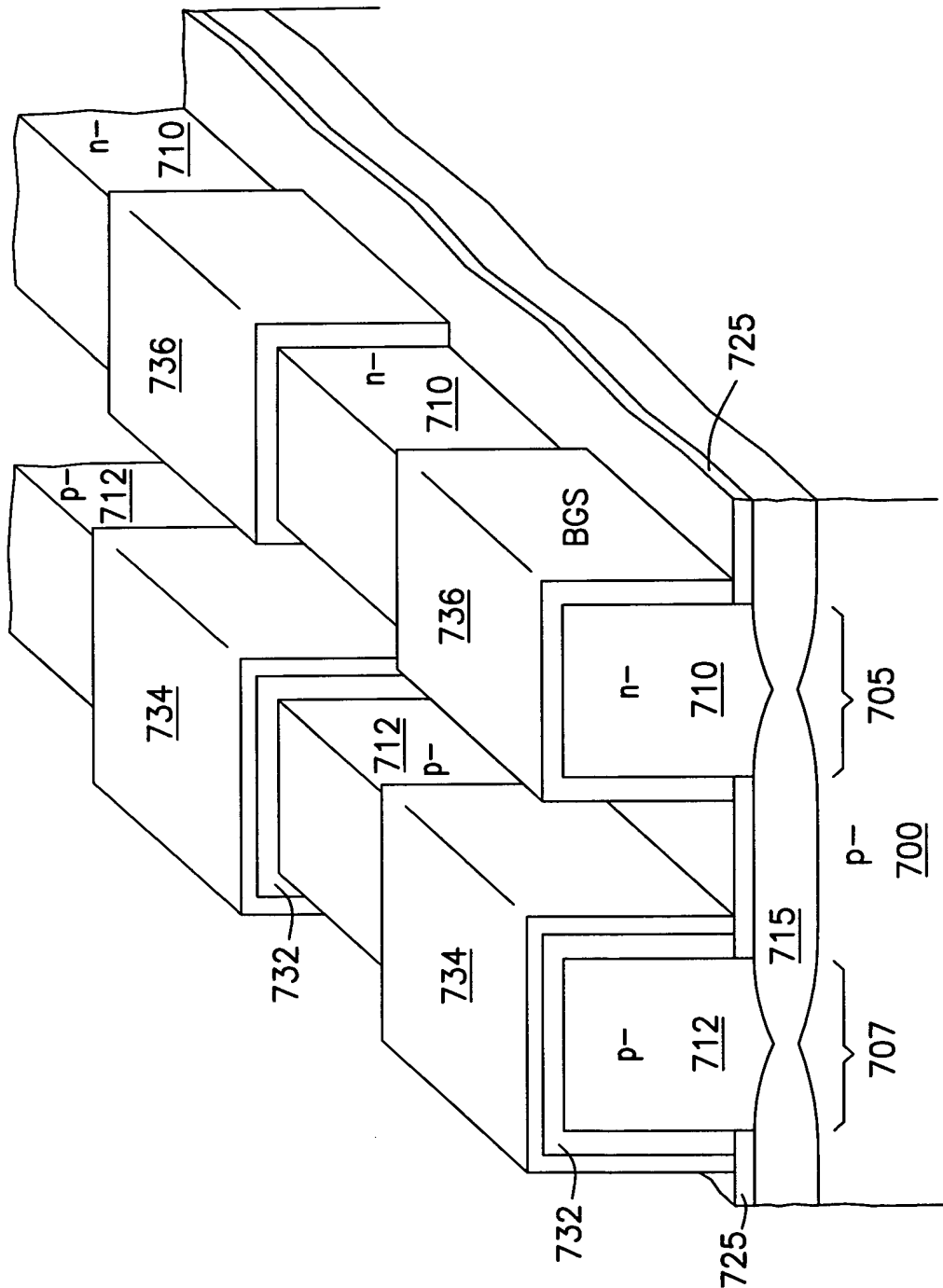


FIG. 6E







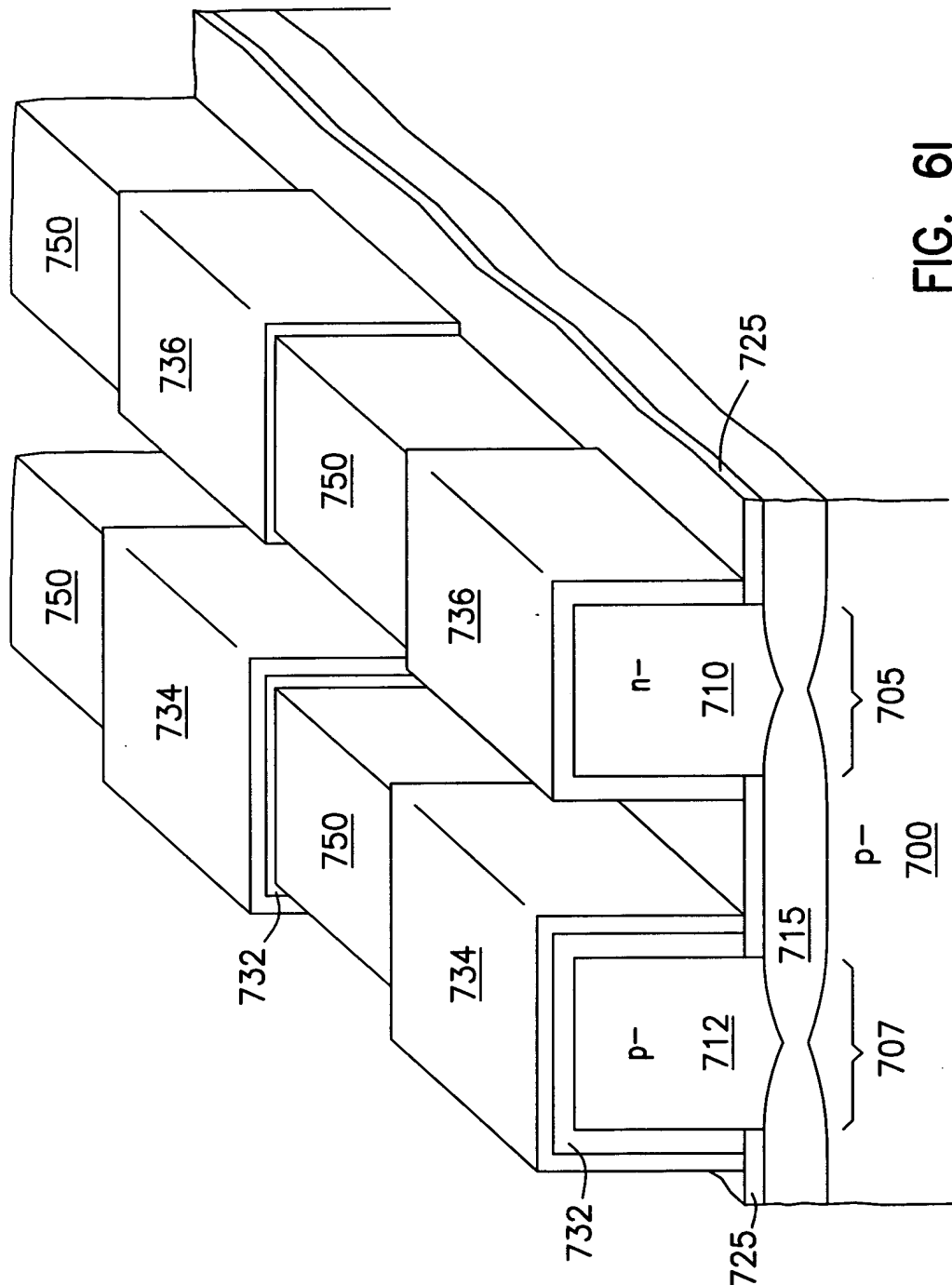
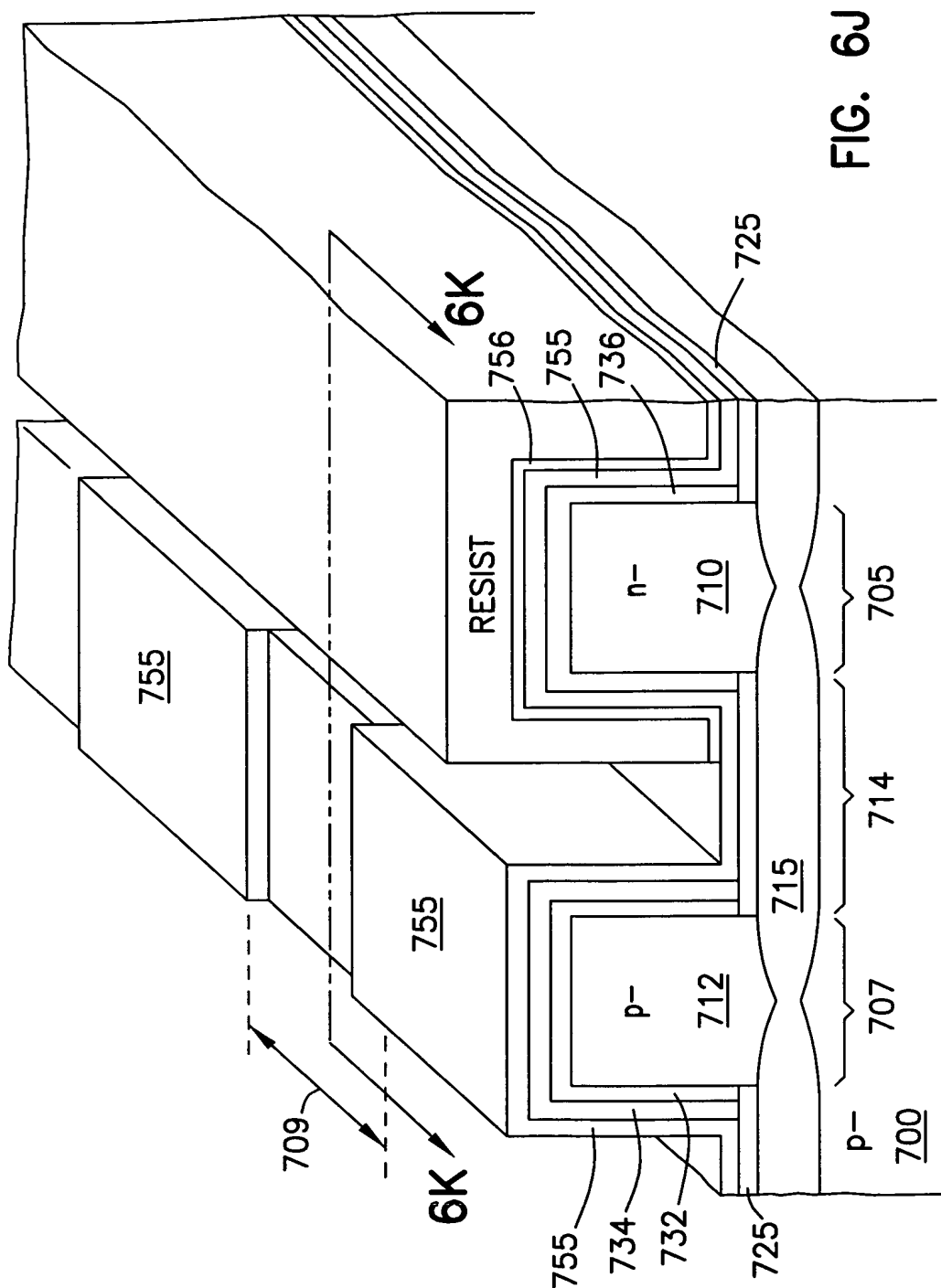
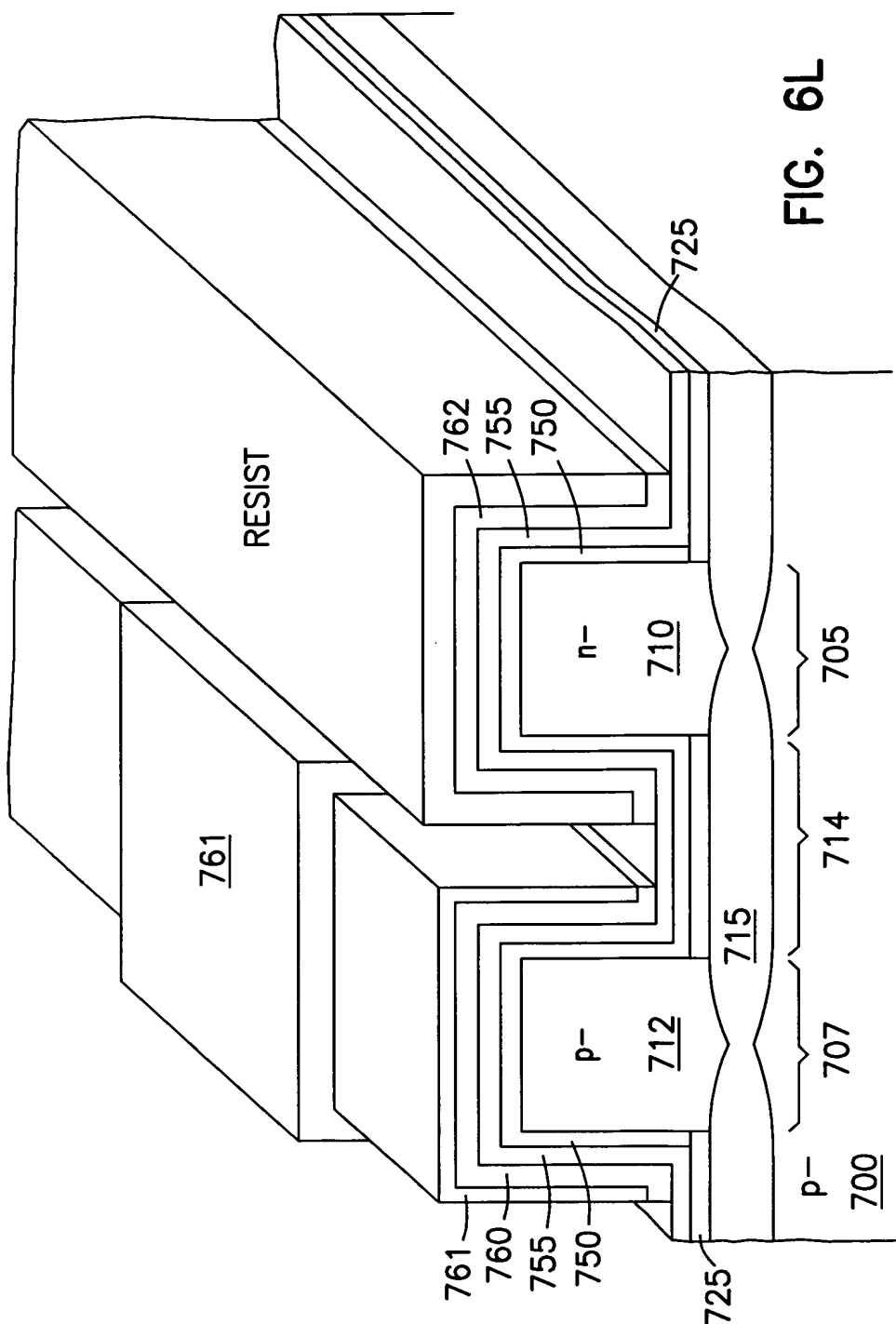
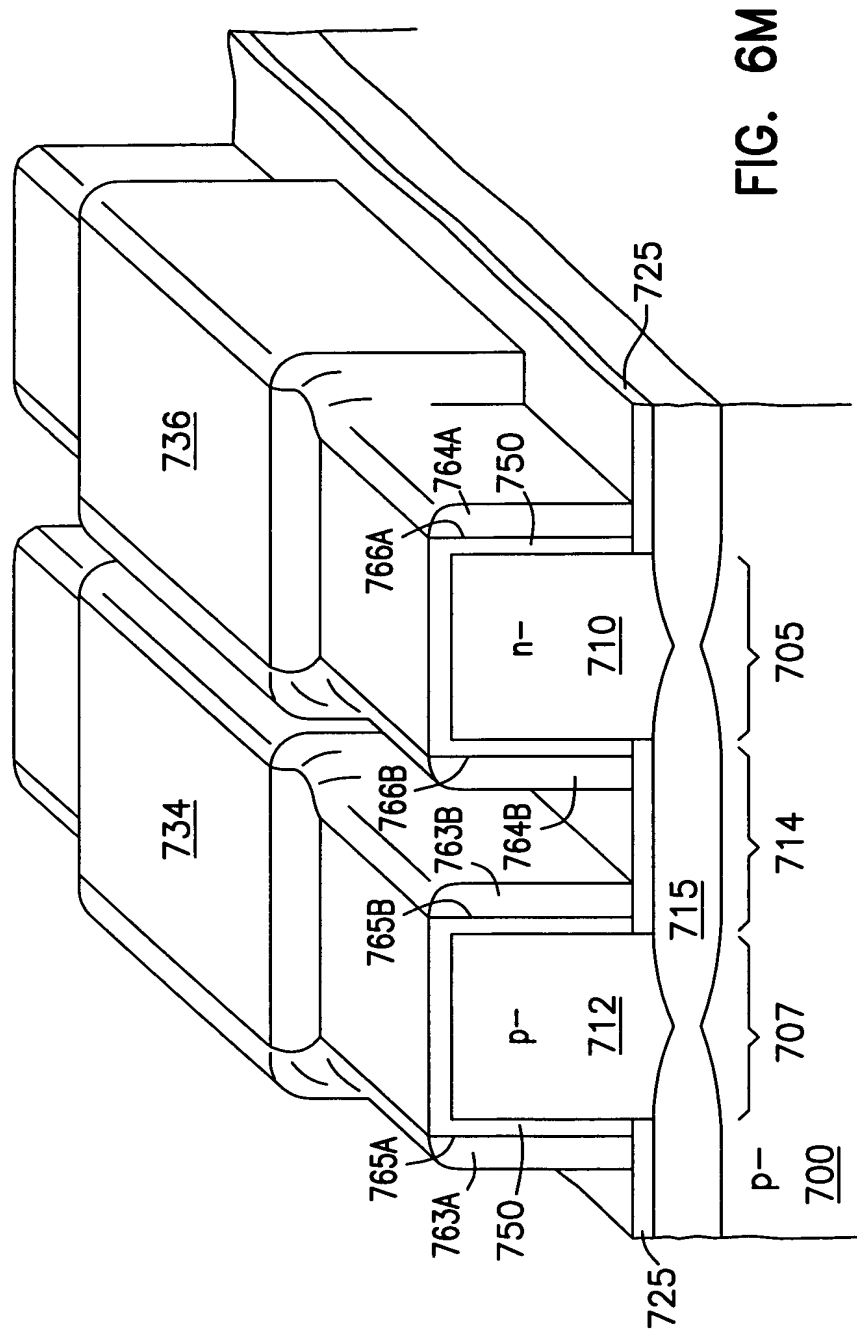


FIG. 61









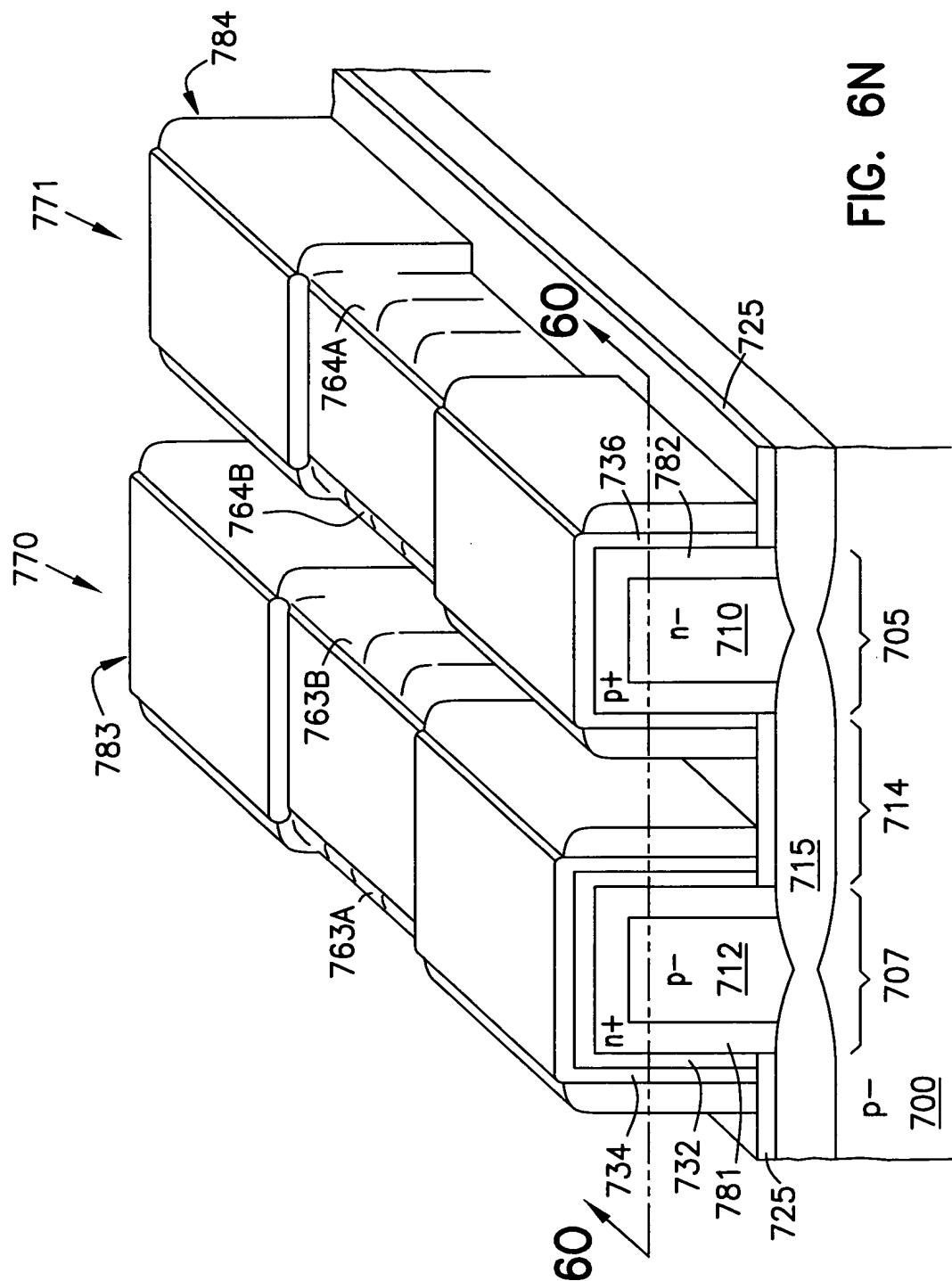


FIG. 6N

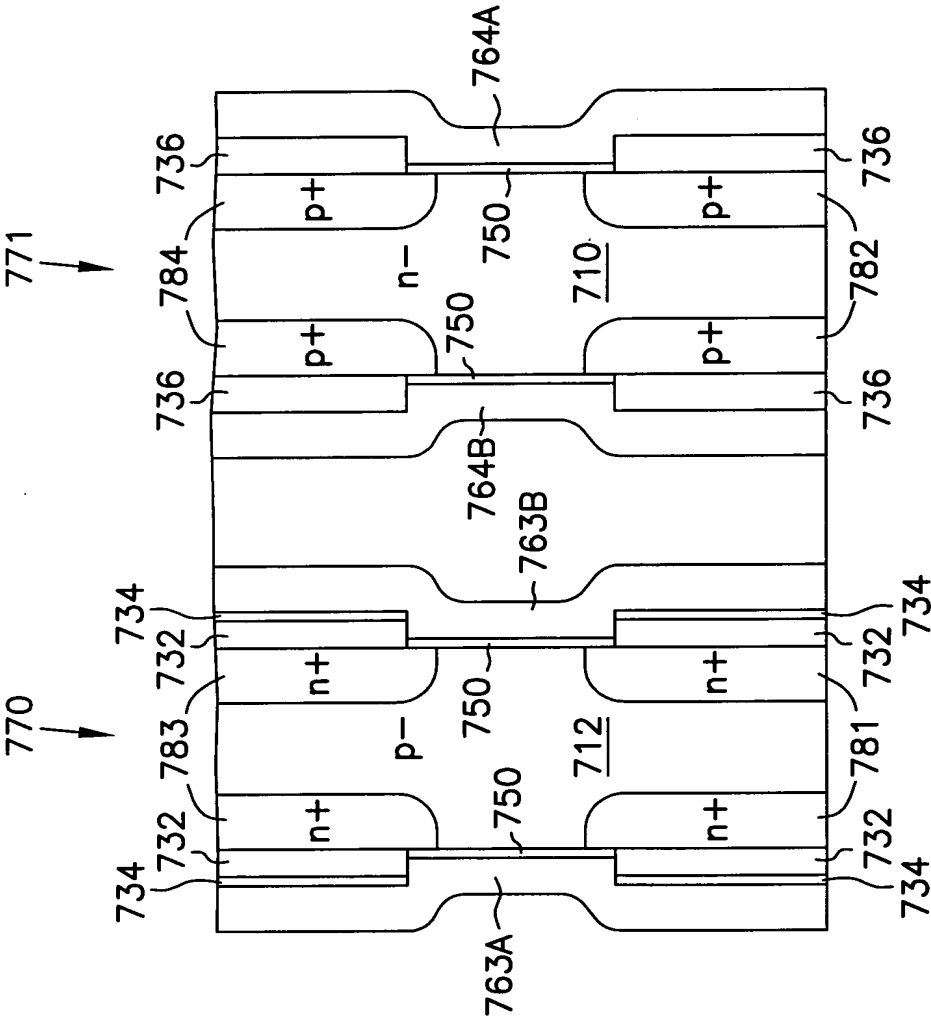


FIG. 60

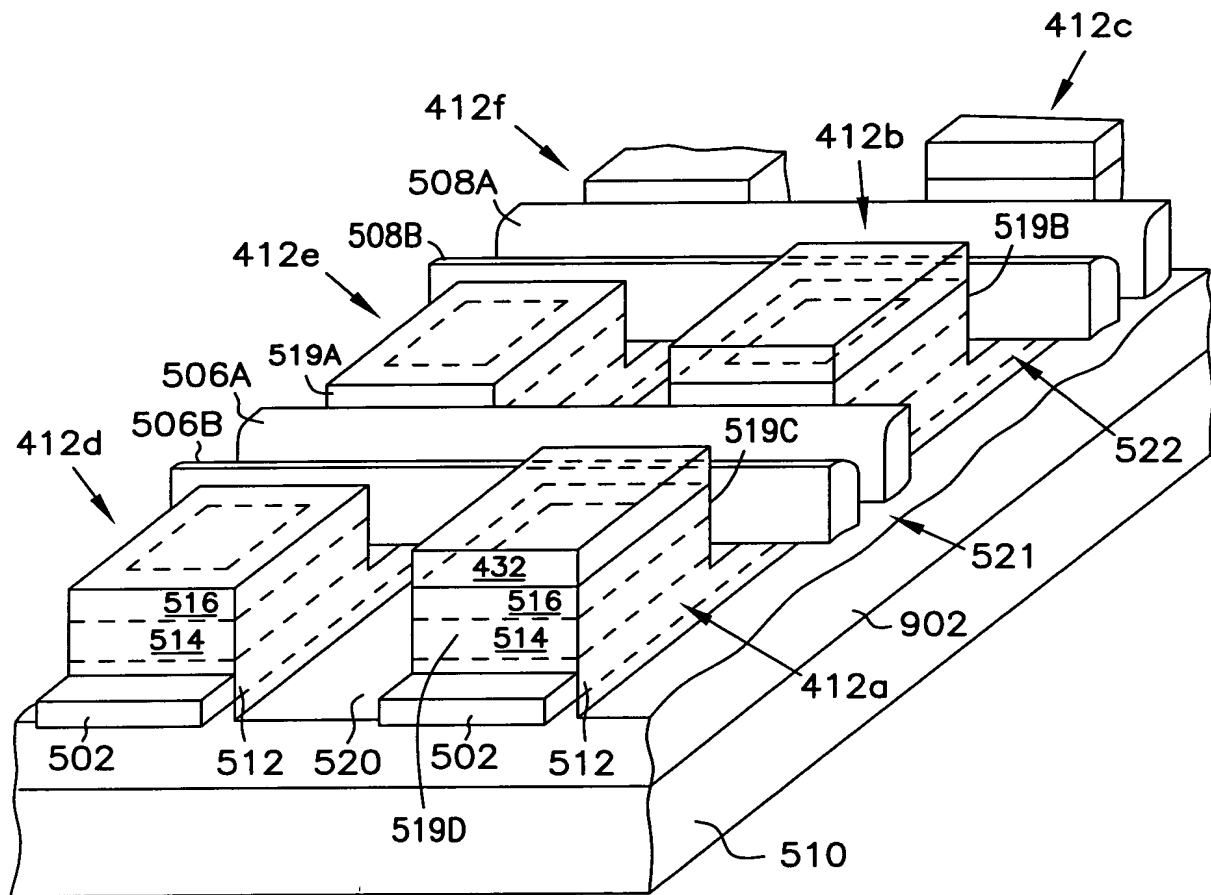
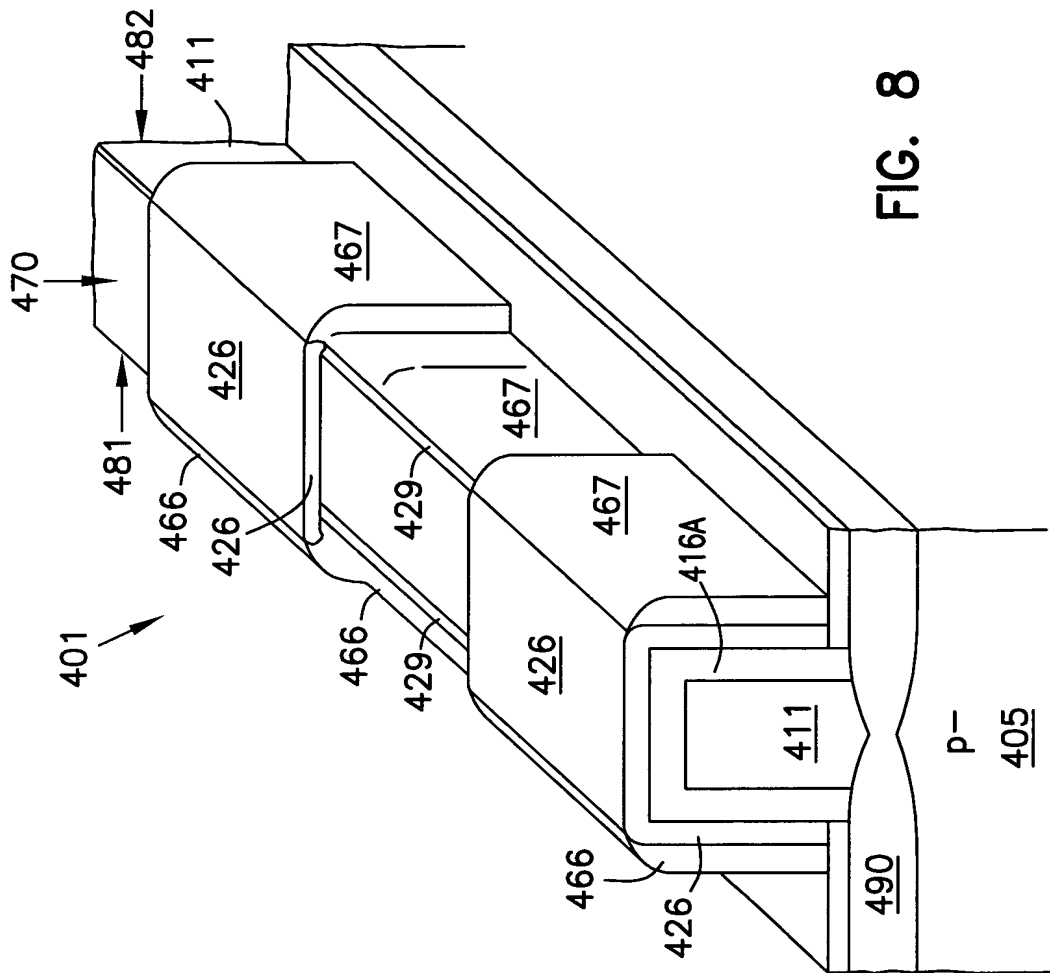


FIG. 7



26/26

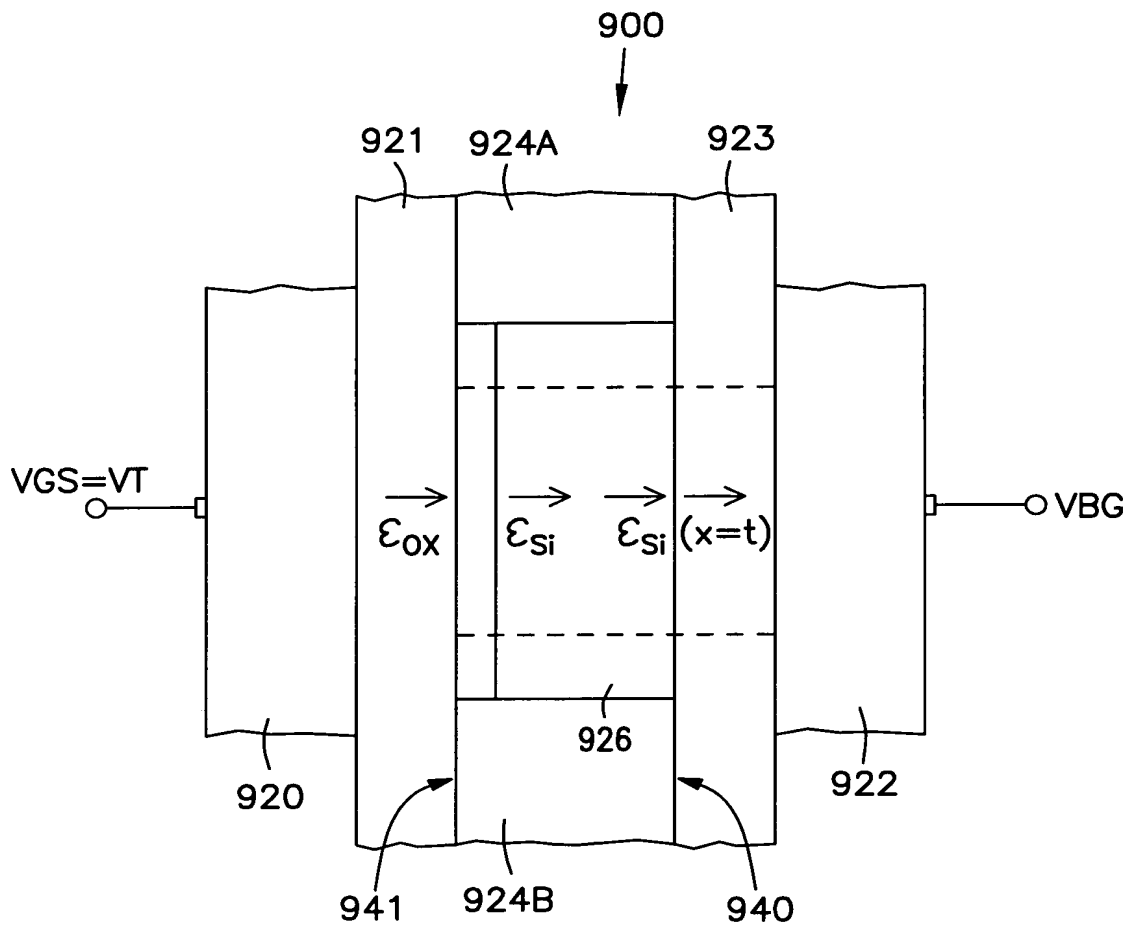


FIG. 9